

**TRANSMITTAL LETTER TO THE UNITED STATES  
DESIGNATED/ELECTED OFFICE (DO/EO/US)  
CONCERNING A FILING UNDER 35 U.S.C. 371**

3672-0111P  
U.S. APPLICATION NO. (If known, see 37 CFR 1.5)  
**09/763948**  
NEW

INTERNATIONAL APPLICATION NO. PCT/NO00/00228	INTERNATIONAL FILING DATE June 30, 2000	PRIORITY DATE CLAIMED June 30, 1999
---	--	--

**TITLE INVENTION**  
**A METHOD FOR ELECTRICAL CONTACTING OR ISOLATION OF ORGANIC OR INORGANIC SEMICONDUCTORS AND \***  
**APPLICANT(S) FOR DO/EO/US**

JACKSON, Thomas; WANG, Jianna

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39 (1).
4. ☒ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
  - a. ☒ is transmitted herewith (required only if not transmitted by the International Bureau).
  - b. ☒ has been transmitted by the International Bureau. WO 01/01502
  - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☐ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
  - a. ☐ is transmitted herewith.
  - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4)
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)).
  - a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
  - b. ☐ have been transmitted by the International Bureau.
  - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
  - d. ☒ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ An English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).



Items 11. to 20. below concern document(s) or information included:

11. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98-International Search Report (PCT/ISA/210) w/ 8 documents
12. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A **FIRST** preliminary amendment.
14. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
15. ☐ A substitute specification.
16. ☐ A change of power of attorney and/or address letter.
17. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821-1.825.
18. ☐ A second copy of the published international application under 35 U.S.C. 154(d)(4).
19. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
20. ☒ Other items or information:
  - 1.) PCT Request (PCT/RO/101)
  - 2.) Four (4) sheets of Formal Drawings

\*A METHOD FOR ITS FABRICATION

U.S. APPLICATION NO. (if known, give 37 CFR 1.41) <b>097/NEW 3948</b>		INTERNATIONAL APPLICATION NO. <b>PCT/NO00/00228</b>		ATTORNEY'S DOCKET NUMBER <b>3672-0111P</b>							
21. <input checked="" type="checkbox"/> The following fees are submitted: <b>BASIC NATIONAL FEE (37 CFR 1.492(a)(1)-(5):</b> Neither in international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO. .... <b>\$1,000.00</b> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO. .... <b>\$860.00</b> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO. .... <b>\$710.00</b> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4). .... <b>\$690.00</b> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4). .... <b>\$100.00</b> <b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				<b>CALCULATIONS</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th style="width: 50%;">CALCULATIONS</th> <th style="width: 50%;">PTO USE ONLY</th> </tr> <tr> <td style="text-align: right;">\$ 1000.00</td> <td></td> </tr> <tr> <td style="text-align: right;">\$ 130.00</td> <td></td> </tr> </table>		CALCULATIONS	PTO USE ONLY	\$ 1000.00		\$ 130.00	
CALCULATIONS	PTO USE ONLY										
\$ 1000.00											
\$ 130.00											
Surcharge of \$130.00 for furnishing the oath or declaration later than <input checked="" type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)).				\$ 130.00							
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE								
Total Claims	24 - 20 =	4	X \$18.00	\$	72.00						
Independent Claims	2 - 3 =	0	X \$80.00	\$	0						
MULTIPLE DEPENDENT CLAIM(S) (if applicable) None			+ \$270.00	\$	0						
<b>TOTAL OF ABOVE CALCULATIONS =</b>				\$	1202.00						
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.				\$	0						
<b>SUBTOTAL =</b>				\$	1202.00						
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				\$	0						
<b>TOTAL NATIONAL FEE =</b>				\$	1202.00						
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				\$	0						
<b>TOTAL FEES ENCLOSED =</b>				\$	1202.00						
				Amount to be: refunded	\$						
				charged	\$						
a. <input checked="" type="checkbox"/> A check in the amount of \$ <b>1202.00</b> to cover the above fees is enclosed. b. <input type="checkbox"/> Please charge my Deposit Account No. _____ in the amount of \$ _____ to cover the above fees. A duplicate copy of this sheet is enclosed. c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <b>02-2448</b> . <b>NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.</b> Send all correspondence to: <b>Birch, Stewart, Kolasch &amp; Birch, LLP or Customer No. 2292</b> <b>P.O. Box 747</b> <b>Falls Church, VA 22040-0747</b> <b>(703)205-8000</b> <b>Date: February 28, 2001</b>											
By				Michael K. Mutter, #29,680							

PATENT  
3672-0111P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: JACKSON, Thomas et al. Conf.:  
Int'l. Appl. No.: PCT/NO00/00228  
Appl. No.: New Group:  
Filed: February 28, 2001 Examiner:  
For: A MEANS FOR ELECTRICAL CONTACTING OR  
ISOLATION OF ORGANIC OR INORGANIC  
SEMICONDUCTORS AND A METHOD FOR ITS  
FABRICATION

PRELIMINARY AMENDMENT

**BOX PATENT APPLICATION**

Assistant Commissioner for Patents  
Washington, DC 20231

February 28, 2001

Sir:

The following Preliminary Amendments and Remarks are respectfully submitted in connection with the above-identified application.

AMENDMENTS

IN THE SPECIFICATION:

Please amend the specification as follows:

Before line 1, insert --This application is the national phase under 35 U.S.C. § 371 of PCT International Application No. PCT/NO00/00228 which has an International filing date of June 30, 2000, which designated the United States of America.--

09763948-060801

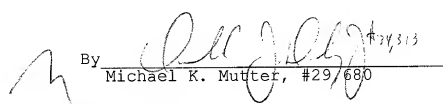
REMARKS

The specification has been amended to provide a cross-reference to the previously filed International Application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By   
Michael K. Mutter, #29,680

P.O. Box 747  
Falls Church, VA 22040-0747  
(703) 205-8000

MKM/cgc  
3672-0111P

(Rev. 02/12/01)

09763948-06004  
108090-84659260

**A means for electrical contacting or isolation of organic or inorganic semiconductors and a method for its fabrication.**

The invention concerns a means for electrical contacting or isolation of organic or inorganic semiconductors in electronic and optoelectronic devices, particularly thin-film devices, wherein the means comprises a substrate either in the form of contact material consisting of an organic or inorganic electrical conductor, or in the form of an isolating material consisting of an organic or inorganic dielectric.

The invention also concerns a method for fabricating a means for electrical contacting or isolation of organic or inorganic semiconductors in electronic and optoelectronic devices, particularly thin-film devices, wherein the means comprises a substrate either in the form of contact material consisting of an organic or inorganic electrical conductor, or in the form of an isolating material consisting of an organic or inorganic dielectric.

Electrical contacts in electronic and optoelectronic devices made with inorganic semiconductor material may frequently present problems. The devices, including thin-film transistors and light-emitting devices, often make use of the isolating properties of the inorganic semiconductor materials, for instance in order to provide low current levels in thin-film transistors in the off-state. However, high resistivity in the semiconductor material can make the current injection at the contacts problematic. Generally metals or other conductors with a given work function are used in order to improve the contact properties by reducing the injection barrier, but this has been successful only to a limited degree. Doping of the organic semiconductor medium or local surface doping, occasionally in combination, has also been attempted. It has been shown that doping of oligothiophenes with iodine ( $I_2$ ), iron (III) or chloride (e.g.  $FeCl_3$ ) increases the conductivity of oligothiophene with up to  $0,1 \text{ S cm}^{-1}$  (see for instance S. Hotta & K. Waragai, Journal of Material Chemistry, 1:835 (1991) and D. Fichou, G. Horowitz, X.B. Xu & F. Garnier, Synthetic Metals 41:463 (1991)), and that a doping of this kind can improve the contacts (Y.Y. Lin, D.J. Gundlach & T.N. Jackson, Materials Research Society, Symposium Proceedings, pp.413-418 (1996)). However, it is difficult to achieve selective doping, and the high mobility of ionic dopants ( $I_3^-$  or  $FeCl_4^-$ ) usually results in poor device stability. Organic molecular dopants such as tetracyanoquinodimethane (TCNQ) have also been used (F. Garnier, F. Kouki, R. Hajlaoui & G.

Horowitz, Materials Research Society Bulletin, June 1997, pp. 52-56). A thin layer, e.g. about 4 nm thick, of TCNQ was deposited in vacuum between an organic semiconductor layer and source and drain electrodes of gold in a thin-film transistor. However, organic molecular charge transfer materials, which can be deposited by evaporation or other simple methods, have a poor film-forming property and this limits their application. Nor is it clear that a doping of this kind will be significantly more stable than inorganic doping. In addition it is necessary with lithography or other patterning procedures in order to align the charge transfer layers with source/drain contacts of organic thin-film transistors.

The primary object of the present invention is thus to overcome the problems with prior art and provide improved contacts for contacting of organic as well as inorganic semiconductors in electronic and optoelectronic devices, particularly thin-film devices. Particularly it is the object to provide an improved contact without additional patterning of the device layers being necessary, while instabilities due to diffusion and field effects are avoided. Further it is an object of the present invention to provide an isolation of organic or inorganic semiconductors in electronic and optoelectronic devices, particularly a selective isolation in order to reduce and eliminate leakage current in an electronic semiconductor layer outside the active area in the device or in order to reduce the effective channel length in organic or inorganic field effect transistors realized in thin-film technology.

The above-mentioned objects are achieved according to the invention with a means which is characterized in that it further comprises a charge transfer material provided patterned or unpatterned on or at a surface of the substrate, the charge transfer material including charge transfer components in the form of donors and/or acceptors, that the charge transfer material forms a self-assembling layer of one or more atomic and/or molecular layers, that the charge transfer material has a direct or indirect bond to the surface of the substrate, and that the charge transfer material forms a charge transfer complex with a thereabove adjacently provided organic or inorganic semiconductor, the charge transfer material forming a donor or acceptor material in the charge transfer complex depending upon respectively whether the semiconductor itself is an acceptor or donor material.

Preferably the bond to the surface of the substrate is a chemical or electrostatic bond or a combination thereof.

In a first embodiment of the means according to the invention, the charge transfer material is an organic compound and may preferably comprise a functional group which forms the bond to the surface of the substrate. Preferably the functional group can be material selective and form the bond to a specific substrate material.

In another embodiment of the means according to the invention, wherein the charge transfer material is provided at the surface of the substrate, the means comprises a connection layer without charge transfer components provided between the surface of the substrate and the charge transfer material, the connection layer forming a bond to the surface of the substrate and a bond to the charge transfer material.

Preferably is then the bond in each case a chemical or electrostatic bond or a combination thereof. The connection layer can preferably be formed of an organic bonding agent and particularly the organic bonding agent can be formed of DNA molecules, such that the one half strand of a DNA molecule is bonded to the surface of the substrate and the complementary second half strand of the DNA molecule is bonded to the charge transfer material.

In an advantageous variant embodiment of the means according to the invention the charge transfer material is an atomic or molecular inorganic compound. Where the charge transfer inorganic compound is provided on the surface of the substrate, the inorganic compound is then preferably formed of a material which reacts chemically with the substrate and between the substrate and the inorganic compound forms a connection layer consisting of a chemical compound of the substrate material and the inorganic compound. If the charge transfer inorganic compound is provided at the surface of the substrate, the means then preferably comprises a connection layer between the substrate and the inorganic compound, the connection layer consisting of a chemical compound of the substrate material or a material with similar chemical properties, and the charge transfer inorganic compound.

A method for fabricating the means according to the invention is characterized by providing a charge transfer material as a patterned or unpatterned self-assembling layer of one or more atomic and/or molecular

layers on or at a surface of the substrate, the charge transfer material including charge transfer components in the form of donors and/or acceptors, forming a direct or indirect bond between the charge transfer material and the surface of the substrate, and forming a charge transfer complex of the charge transfer material together with a thereabove adjacently provided organic or inorganic semiconductor, the charge transfer material forming a donor or acceptor material in the charge transfer complex depending upon respectively whether the semiconductor itself is an acceptor or donor material.

Preferably the bond is formed in the method according to the invention as a chemical or electrostatic bond or a combination thereof.

In a first embodiment of the method according to the invention the charge transfer material advantageously is selected as an organic compound, preferably with a functional group which forms the bond to the surface of the substrate. Preferably the functional group can be a material-selective group such that the bond is formed to a specific substrate material.

In a second embodiment of the method according to the invention, wherein the charge transfer material is provided at the surface of the substrate, a connection layer without a charge transfer component is provided between the surface of the substrate and the charge transfer material, the connection layer being formed with a bond to the surface of the substrate and with a bond to the charge transfer material. Preferably the bond in each case is formed as a chemical or electrostatic bond or a combination thereof.

The connection layer can advantageously be formed of an organic bonding agent and particularly the organic bonding agent can be formed of DNA molecules, such that the one half strand of DNA molecule is bonded to the surface of the substrate and the complementary second half strand of the DNA molecule is bonded to the charge transfer material.

In an advantageous variant embodiment of the method according to the invention, the charge transfer material is advantageously selected as an atomic or molecular inorganic compound. Where the charge transfer inorganic compound is provided on the surface of the substrate, the inorganic compound is then preferably formed of an material which reacts chemically with the substrate, such that between the substrate and the inorganic compound a connection layer consisting of a chemical compound of the



substrate material and the inorganic compound is formed. Where the charge transfer inorganic compound is provided at the surface of the substrate, a connection layer consisting of a compound of the substrate material or a material with similar chemical properties and the inorganic compound is preferably provided between the substrate and the inorganic compound.

The present invention shall now be explained in more detail with reference to exemplary embodiments and in connection with the appended drawings, wherein

fig. 1 shows schematically a self-assembling charge transfer molecule on a substrate,

fig. 2a-e the structure of various organic charge transfer compounds,

fig. 3 a schematic section through the means according to the invention used in a thin-film transistor,

fig. 4 a schematic section through a thin-film transistor with the means according to the invention,

fig. 5 a schematic section through an organic light-emitting diode in thin-film technology, wherein the means according to the invention is used,

fig. 6 a schematic section through a portion of a thin-film transistor, wherein the means according to the invention is used,

fig. 7 a schematic section through a portion of a thin-film transistor, wherein the means according to the invention is used for reducing current leakage, and

fig. 8a the current-voltage characteristics of an organic thin-film transistor according to prior art, and

fig. 8 b the current-voltage characteristics of an organic thin-film transistor with the means according to the invention.

First the background of the invention shall briefly be explained. A number of aromatic and other organic molecules may form donor complexes with different compounds. Molecules which are capable of giving up electrons are electron donors. For instance, aromatic hydrocarbons, including alkenes and alkyls, which have  $\pi$  orbitals, are donor molecules in many systems.

Molecules which are capable of accepting electrons, are electron acceptors. Aromatic nitro compounds and quinones are  $\pi$  acceptors and halogen molecules with vacant  $\sigma$  antibonding orbitals act as  $\sigma$  acceptors in many systems. For instance can aromatic hydrocarbons such as tetracene and pentacene act as electron donors towards benzoquinones or trinitrobenzene. The effect of introducing a charge donor or charge acceptor in an organic semiconductor corresponds to introducing charge-donating or charge-accepting impurities in an organic semiconductor (K. Tamaru & M. Kchikawa, "Catalysis By Electron Donor-Acceptor Complexes", Halsted Press, New York (1975)). It shall be remarked that charge transfer often depends on the molecular environment and a single molecule species can sometimes act as a donor or an acceptor depending on the organic semiconductor being considered. In addition it is to be remarked that donor and acceptor materials in no way are limited to organic compounds. There are known inorganic charge transfer materials, including iodine ( $I_2$ ), iron (III) or ferrichloride ( $FeCl_3$ ) such as mentioned in the introduction. These may be used when they are given a suitable bond to for instance a contact material.

The means according to the invention can be used both with substrates which are electrical conducting, for instance contact materials as used in thin-film transistors or also, for specific applications, with substrates of a dielectric material, something which shall be mentioned later.

A suitable charge transfer material whose molecules or for the sake of that atoms, may act as donor or acceptors depending on the circumstances, is used to provide local doping of for instance one or more contact areas in a semiconductor device realized in thin-film technology. The means according to the invention achieves good stability by the charge storage components being attached to the contact material with a bond which for instance may be chemical, electrostatic or another suitable bonding mechanism, possibly combinations of several such bonding mechanisms. Basically this may according to the invention be achieved in two different ways.

In a first method the charge transfer material are used in the form of a compound which for instance forms a chemical bond to the substrate surface. In some cases a charge transfer compound of this kind will form a self-assembling monolayer (SAM). This may can used for minimizing the thickness of layers of charge transfer material, but is not essential in order to

form contact areas which are locally doped with charge transfer material. Fig. 1 shows schematically a charge transfer molecule 2 bound to a substrate 1, for instance a metal surface. The functional head group 2' in the charge transfer molecule 2 then forms a chemical bond 2'' with a surface 1.

5 Figs. 2a-f show some examples of charge transfer organic compounds with a functional head group. Here the bonds are respectively F, Cl or NO<sub>2</sub>, and X denotes respectively -NC or -SH.

Fig. 2a shows the structure of 4,4'-substituted phenyl, fig. 2b the structure of 4,4'-substituted biphenyl, fig. 2c shows the structure of 4,4'-substituted phenylethynyl benzene, fig. 2d shows the structure of substituted naphthalene, fig. 2e shows the structure of substituted benzimidazole and finally fig. 2f shows the structure of 2-mercapto 5-nitrobenzimidazole which is a mercaptan or thiol compound with -SH as functional head group.

For different metal surfaces different functional groups may be used for forming the bond. For instance can mercapto and thiol groups as shown in fig. 2e and particularly in fig. 2f, where the mercapto or thiol group are -SH, form strong bonds to surfaces of gold, silver and copper. For platinum may amines (-NH<sub>2</sub>) or isonitriles (-NC) be preferred as they can easily form charge transfer bonded layers on a substrate of this kind (A. Ulman, "An Introduction to Ultrathin Organic Films", Academic Press, Inc. (1991)). It may be mentioned that a large number of materials have been investigated with regard to use as donor or acceptor materials and a large number of compounds which may be used as charge transfer material exists or can be synthesized (see e.g. K. Tamaru and M. Kchikawa, op.cit., and J.E. Katon, "Organic Semiconducting Polymers" Marcel Dekker, Inc., New York (1968)).

The embodiment of the method according to the invention with choosing a charge transfer compound with a functional group which can be bonded directly to a metal surface is simple, but may in some cases limit the choice of charge transfer compounds.

An alternative embodiment of the method according to the invention is hence to first form a connection layer without charge transfer components on the substrate and then to bond the charge transfer components or compounds to this connection layer. This opens for a large number of possibilities for

different connection layers and schemes for providing a suitable bond. Typically there may for instance be desirable with a covalent bond to a metal surface and the charge transfer compound may for instance be bonded chemically or electrostatically. In an advantageous variant of the embodiment the one half strand of a DNA molecule is bonded to the substrate. The complementary second half strand of the DNA molecule can afterwards be bonded to the charge transfer molecule and will then form a strong bond to the DNA molecule on the substrate.

The embodiment of the means according to the invention where a charge transfer material 2 is used for improving the current injection of the source or drain electrode in inorganic thin-film transistors is particularly shown in fig. 3. The charge transfer compound may for instance be 2-mercapto 5-nitrobenzimidazole (MNB), and the organic thin-film transistor may be made with pentacene as the active semiconductive material. The contacts themselves may be made of gold. In fig. 3 is the MNB molecule 2 shown provided on the source and drain contacts 1a, which in their turn are provided on the gate isolator 4 of the gate electrode 5. The organic semiconductor material is left out in fig. 3. The functional group S forms the bond between the MNB molecule 2 and the gold surface. In this case S, of course, is a mercapto or thiol group -SH. The MNB molecules 2 form as shown in fig. 3 a self-assembling monolayer of MNB material, the layer being restricted to the gold electrodes 1a and is only present there, as shown in fig. 3. The surface is now ready for deposition of the organic semiconducting material, i.e. pentacene, and the circuit can then be completed in the usual manner.

Fig. 4 shows a thin-film transistor where the source and drain contacts 1a are locally doped with an immobilized layer 3 of charge transfer material which forms the charge transfer complex of acceptor or donor materials, i.e. of the charge transfer compound and the active, in this case organic semiconductor 6 which is provided over both the source and drain contacts 1a and the layer of charge transfer material. A gate isolator 4, e.g. of silicon dioxide, provides isolation against the gate electrode 5 which in its turn may be formed by the silicon chip. It shall be understood that the charge transfer material 3 used equally well may be of a species which does not form the layer as a monolayer, but instead as a number of separate atomic and/or molecular layers.

Above the means according to the invention is specifically discussed used in organic thin-film transistors. Improved contacts are of course of great interest for a large number of organic devices and not only restricted to organic thin-film transistors. As examples may be mentioned organic light-emitting diodes, various other organic diodes, organic photovoltaic devices and organic sensors and a large number of other organic electronic and optoelectronic devices. For instance, fig. 5 shows schematically a section through a light-emitting diode where a layer 3 of a charge transfer material is provided between the cathode 7 and the organic semiconductor material 6. Further an additional layer 3 of charge transfer material is provided between the semiconductor 6 and the anode 8, the anode in its turn being provided on a suitable substrate material 9. The cathode 7 can be made of a transparent material. Possibly it can be the anode 8 and the substrate 9 which are made of a transparent material. The layers 3 of charge transfer material will normally let light through, as they at most will have a thickness of the magnitude one or a few molecules. It is, of course, to be understood that the layer thickness in fig. 5 as in the remaining figures is not to scale. Typically will the organic semiconducting material, however, form a much thicker layer than the charge transfer material, namely of the magnitude from a few ten nanometers and up to several hundred nanometers.

The means according to the invention is not restricted to comprise an electric contact material, for instance metal, but may also be used for forming charge transfer complexes with a semiconductor material outside the contact areas. This presupposes that the charge transfer material can be bonded to an electrical isolating material, i.e. in practice a dielectric. A bond between a charge transfer material and a dielectric may e.g. be used to displace the threshold voltage either in the positive or negative direction in a field-effect transistor. In a p-channel transistor an acceptor-like charge transfer material will for instance displace the threshold voltage in negative direction, and a donor-like charge transfer material will displace the threshold voltage in a positive direction.

As shown in fig. 6 the use of a layer 3 of charge transfer material in the channel area can be used to reduce the effective channel length  $L_{eff}$ . This corresponds to a reduction of the channel length in for instance field-effect transistors based on single crystal silicon, amorphous silicon or polysilicon. The doped areas will then provide a low resistance access to the channel area

of the transistor. This will be particularly useful in light-emitting semiconductor devices where doping with a charge transfer material shall allow contacting without using a conductor which might absorb light or reduce the performance of an organic light-emitting diode. Fig. 6 shows specifically and schematically a field-effect transistor in a thin-film technology, where a thin layer 3 of charge transfer material is provided in the channel area between the source and drain contacts and bonded to the isolating material 4 which forms the gate isolator. Simultaneously, the charge transfer material 3 also contacts the active semiconductor 6 in the channel area. The result of forming such an immobilized local doping layer of a charge transfer complex is that the lithographically defined channel length  $L_{def}$  now is reduced to an effective channel length  $L_{eff}$  as shown.

Fig. 7 shows an embodiment of the means according to the invention wherein layers 3 of charge transfer material are provided on the isolating material 4 outside the contact areas and form a charge transfer complex with the thereabove provided semiconducting layer 6. This may contribute to a better isolation of the semiconductor device and prevent undesired leakage currents. If the isolating material 4 e.g. is formed of silicon dioxide, silane can be used as bonding agent between the charge transfer material and the silicon dioxide.

According to the invention the inorganic charge transfer material may be used with a connection layer where the bonding agent is inorganic. An example is a charge transfer material in the form of arsenic or phosphor which respectively is bonded with an arsenide or phosphide layer to the underlying contact material. This may also be done directly, for instance by the contact material being a metal, e.g. copper which forms an arsenide or phosphide with respectively a charge transfer material in the form of arsenic or phosphor. Arsenic or phosphor between the contact material and the semiconductor will be bonded to the former, but yet be able to form a charge transfer complex which provides charge carriers for the semiconductor employed.

The charge transfer material may be atomic or molecular, and even if the charge transfer material together with the bond in most cases will appear as a molecular material, it is yet possible to apply atomic materials which may both provide charge transfer and useable bonds. The use of e.g. arsenic or

phosphor as mentioned above are examples of atomic materials in elemental form which can be bound both to a substrate and be used as a charge transfer material.

Even though the above-mentioned examples are directed to thin-film devices with organic semiconductors, the present invention can also be used with inorganic semiconductors. A number of charge transfer molecules and functional groups are stable at temperatures which are used in the fabrication of inorganic semiconductor devices, and the means and the method according to the invention may hence be used in such devices, including devices based on amorphous silicon. Particularly the charge transfer material can be an inorganic material, for instance one of the above-mentioned.

In the means according to the invention a strong bond will be desirable. Usually the bond will be chemical, but a number of chemical bonds may have ionic or electrostatic component and in some cases will perhaps the electrostatic bond be dominating, e.g. if a polyelectrolyte material is used. As mentioned above, the organic semiconductors need not exclusively act as donors or acceptors, but can be respectively one or the other, depending on the characteristics of the charge transfer material. For instance has an organic semiconductor such as pentacene both electrons and holes as free carriers, even though up to now only hole-based devices have shown usable electrical characteristics. It might hence be used charge transfer materials which can be both acceptors or donors in a charge transfer complex with pentacene. It is also known that a charge transfer material which can be an acceptor together with one kind of organic semiconductor can be a donor together with another.

Further it is to be understood that the concept self-assembling as used in connection with mono- or multilayers of a charge transfer material does not imply that the charge transfer material forms a well-ordered layer, but that the material is assembling on a contact area or another desired area. Generally the means according to the invention does not require a regular two-dimensional structure in the self-assembling layer, even though some charge transfer materials will provide this. It may also be mentioned that it will be possible to bond a charge transfer material selectively to a specific material type, for instance a contact material or a dielectric material. This may for instance be achieved by using charge transfer compounds with material-selective functional groups. Combined with patterning by means of

conventional lithographic methods, there can thus be provided selective local or patterned doping with a charge transfer material which in such a case only will be attached in exposed areas in the substrate used. The method according to the invention may in other words be used in combination with conventional lithography, even though the self-assembling property of the charge transfer material makes patterning with the use of lithography unnecessary in most cases.

The formation of a charge transfer complex in the means according to the invention reduces contact resistance or increases the injection efficiency and can increase the external field-effect carrier mobility and improve other characteristics in organic thin-film transistors. The means according to the invention may also improve the efficiency of organic light-emitting diodes or reduce their turn-off voltage.

In order to investigate the effect of using an immobilized local doping with the use of charge transfer materials, organic thin-film transistors were made where the charge transfer material acted as acceptor material. It was also made such transistors respectively without use of charge transfer material and where the charge transfer material acted as a donor material. It was expected that the charge transfer material with the acceptor properties would improve the performance of the thin-film transistor and the charge transfer material with donor properties reduce the performance thereof. This was confirmed experimentally. Transistors where the contacts were treated with an acceptor material had the best transistor performance, transistors where the contacts were treated with a donor material had the poorest performance, and transistors with untreated contacts had a performance intermediate to the other two.

Pentacene-based organic thin-film transistors with gold contacts were made with an immobilized charge transfer material of the acceptor type on the contacts. The charge transfer material used was in this case MNB. As control also similar transistors were made without charge transfer material. The transistors had a channel width  $W$  of 220  $\mu\text{m}$  and a channel length  $L$  of 30  $\mu\text{m}$ . A gate isolator of silicon oxide with a thickness of 253 nm/TMS and 50 nm thick contacts of gold as drain/source electrode were used. As pentacene-based organic thin-film transistors with gold contacts are hole-transporting, it was expected that the use of an acceptor material would



improve the contacts by providing a local hole concentration. This was confirmed experimentally. Fig. 8a shows the  $I_d$ - $V_{ds}$  characteristics of thin-film transistors with pentacene, but without MNB for different values of the gate-source voltage  $V_{gs}$ , namely 0, -10, -20, -30 and -40 V. Fig. 8b shows the  $I_d$ - $V_{ds}$  characteristics for thin-film transistors with pentacene, but with the use of MNB, for the same values of gate-source voltage  $V_{gs}$  as shown in fig. 8a. From the results it could be deduced that the carrier mobility of the transistors with untreated contacts was  $0,05 \text{ cm}^2/\text{Vs}$ , while it for transistors with MNB-treated contacts was  $0,24 \text{ cm}^2/\text{Vs}$ . In other words, a treatment of the contact material with an acceptor material in this case resulted in higher drain currents and better current saturation.

## PATENT CLAIMS

1. A means for electrical contacting or isolation of organic or inorganic semiconductors in electronic and optoelectronic devices, particularly thin-film devices, wherein the means comprises a substrate (1) either in the form of contact material (1a) consisting of an organic or inorganic electrical conductor or in the form of an isolating material (4) consisting of an organic or inorganic dielectric, characterized in that the means further comprises a charge transfer material (2) provided patterned or unpatterned on or at a surface of the substrate (1), the charge transfer material including charge transfer components in the form of donors and/or acceptors, that the charge transfer material (2) forms a self-assembling layer (3) of one or more atomic and/or molecular layers, that the charge transfer material (2) has a direct or indirect bond to the surface of the substrate (1), and that the charge transfer material (2) forms a charge transfer complex with a thereabove adjacently provided organic or inorganic semiconductor (6), the charge transfer material forming a donor or acceptor material in the charge transfer complex depending upon respectively whether the semiconductor itself is an acceptor or donor material.
2. A means according to claim 1, characterized in that the bond to the surface of the substrate (1) is a chemical or electrostatic bond or a combination thereof.
3. A means according to claim 1, characterized in that the charge transfer material (2) is an organic compound.
4. A means according to claim 1, characterized in that the organic compound (2) comprises a functional group (2') which forms the bond (2'') to the surface of the substrate (1).
5. A means according to claim 4, characterized in that the functional group (2') is material selective and forms the bond (2'') to a specific substrate material (6).
6. A means according to claim 1, wherein the charge transfer material (2) is provided at the surface of the substrate (1), characterized in that the means comprises a connection layer without charge transfer components provided between the surface of the substrate (1) and the charge transfer material (2),

the connection layer forming a bond to the surface of the substrate and a bond to the charge transfer material.

7. A means according to claim 6, characterized in that the bond in each case is a chemical or electrostatic bond or a combination thereof.

8. A means according to claim 6, characterized in that the connection layer is formed of an organic bonding agent.

9. A means according to claim 8, characterized in that the organic bonding agent is formed of DNA molecules, such that the one half strand of a DNA molecule is bonded to the surface of a substrate (1) and the complementary second half strand of the DNA molecule is bonded to the charge transfer material.

10. A means according to claim 1, characterized in that the charge transfer material (2) is an atomic or molecular inorganic compound.

11. A means according to claim 10, wherein the charge transfer inorganic compound (2) is provided on the surface of the substrate (1), characterized in that the inorganic compound (2) is formed of a material which reacts chemically with the substrate (1) and between the substrate (1) and the inorganic compound (2) forms a connection layer consisting of a chemical compound of the substrate material and the inorganic compound.

12. A means according to claim 10, wherein the charge transfer inorganic compound (2) is provided at the surface of the substrate (1), characterized in that the means comprises a connection layer provided between the substrate (1) and the inorganic compound (2), the connection layer consisting of a chemical compound of the substrate material or a material with similar chemical properties, and the charge transfer inorganic compound.

13. A method for fabricating a means for electrical contacting or isolation of organic or inorganic semiconductors in electronic and optoelectronic devices, particularly thin-film devices, wherein the means comprises a substrate either in the form of contact material consisting of an organic or inorganic electrical conductor or in the form of an isolating material consisting of an organic or inorganic dielectric, and wherein the method is characterized by providing a charge transfer material as a patterned or unpatterned self-assembling layer of one or more atomic or molecular layers

on or at a surface of the substrate, the charge transfer material including charge transfer components in the form of donors and/or acceptors, forming a direct or indirect bond between the charge transfer material and the surface of the substrate, and forming a charge transfer complex of the charge transfer material together with a thereabove adjacently provided organic or inorganic semiconductor, the charge transfer material forming a donor or acceptor material in the charge transfer complex depending upon respectively whether the semiconductor itself is an acceptor or donor material.

14. A method according to claim 13, characterized by forming the bond as a chemical or electrostatic bond or a combination thereof.

15. A method according to claim 13, characterized by selecting the charge transfer material as an organic compound.

16. A method according to claim 15, characterized by selecting the organic compound with a functional group which forms the bond to the surface of the substrate.

17. A method according to claim 16, characterized by selecting the functional group as a material-selective group such that the bond is formed to a specific substrate material.

18. A method according to claim 13, wherein the charge transfer material is provided at the surface of the substrate, characterized by providing a connection layer without charge transfer components between the surface of the substrate and the charge transfer material, and forming the connection layer with a bond to the surface of the substrate and with a bond to the charge transfer material.

19. A method according to claim 18, characterized by forming the bond in each case as a chemical or electrostatic bond or a combination thereof.

20. A method according to claim 18, characterized by forming the connection layer of an organic bonding agent.

21. A method according to claim 20, characterized by forming the organic bonding agent of DNA molecules, such that the one half strand of a DNA molecule is bond to the surface of the substrate and the complementary second half strand of the DNA molecule is bond to the charge transfer material.

22. A method according to claim 13, characterized by selecting the charge transfer material as an atomic or molecular inorganic compound.

23. A method according to claim 22, wherein the charge transfer inorganic compound is provided on the surface of the substrate, characterized by forming the inorganic compound of a material which reacts chemically with the substrate such that between the substrate and the inorganic compound a connection layer consisting of a chemical compound of the substrate material and the inorganic compound is formed.

24. A method according to claim 22, wherein the charge transfer inorganic compound is provided at the surface of the substrate, characterized by providing a connection layer consisting of a compound of the substrate material or a material with similar chemical properties, and the inorganic compound, between the substrate and the inorganic compound.

1/4

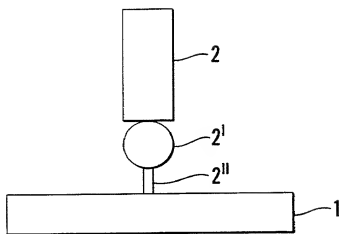


Fig. 1

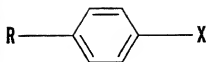


Fig. 2a

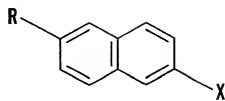


Fig. 2d

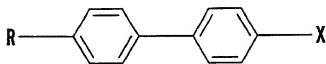


Fig. 2b

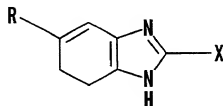


Fig. 2e

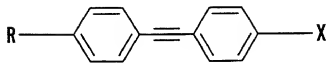


Fig. 2c

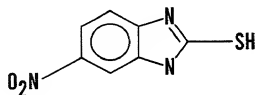


Fig. 2f

2/4

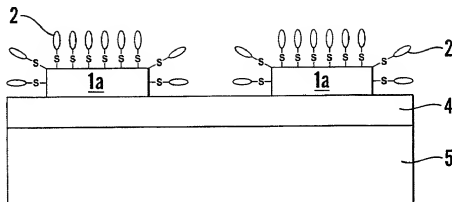


Fig. 3

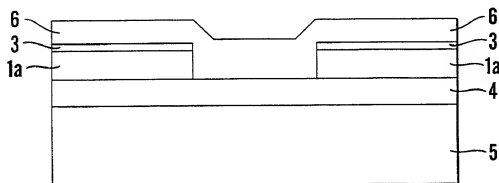


Fig. 4

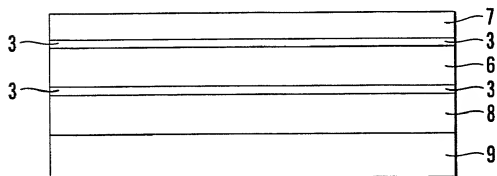


Fig. 5

3/4

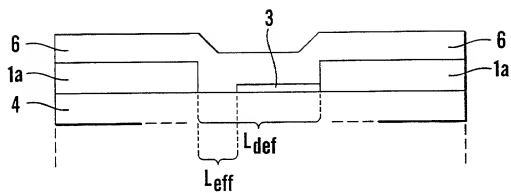


Fig.6

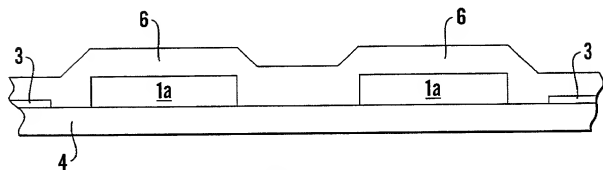


Fig.7



4/4

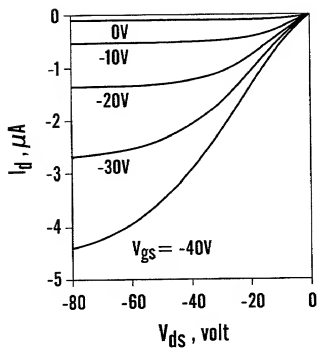


Fig.8a

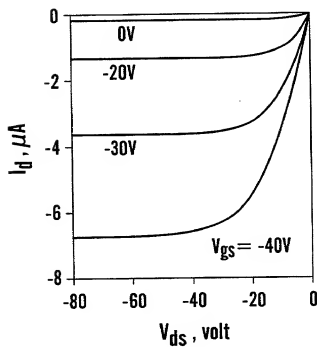


Fig.8b

# BIRCH, STEWART, KOLASCH & BIRCH, LLP

P.O. Box 747 • Falls Church, Virginia 22040-0747  
Telephone: (703) 265-0000 • Facsimile: (703) 265-0050

PLEASE NOTE:  
YOU MUST  
COMPLETE THE  
FOLLOWING

## COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT AND DESIGN APPLICATIONS

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated next to my name; that I verily believe that I am the original, first and sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

A MEANS FOR ELECTRICAL CONTACTING OR ISOLATION OF ORGANIC OR INORGANIC SEMICONDUCTORS AND A METHOD FOR ITS FABRICATION

Insert Title

Fill in Appropriate  
Information -  
For Use Without  
Specification  
Attached:

the specification of which is attached hereto. If not attached herein,  
the specification was filed on February 28, 2001 as  
United States Application Number 09/763,948  
and amended on \_\_\_\_\_ (if applicable) and/or  
the specification was filed on June 30, 2000 as PCT  
International Application Number PCT/NO00/00228 and was  
amended under PCT Article 19 on \_\_\_\_\_ (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representative or assigns more than twelve months (six months for designs) prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representative or assigns, except as follows:

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

### Prior Foreign Application(s)

### Priority Claimed

Insert Priority  
Information  
(if appropriate)

(Number)	(Country)	(Month/Day/Year Filed)	<input checked="" type="checkbox"/> Yes	<input checked="" type="checkbox"/> No
19990266	Norway	June 30, 1999	<input checked="" type="checkbox"/> Yes	<input checked="" type="checkbox"/> No
(Number)	(Country)	(Month/Day/Year Filed)	<input type="checkbox"/> Yes	<input type="checkbox"/> No
(Number)	(Country)	(Month/Day/Year Filed)	<input type="checkbox"/> Yes	<input type="checkbox"/> No
(Number)	(Country)	(Month/Day/Year Filed)	<input type="checkbox"/> Yes	<input type="checkbox"/> No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

Insert Provisional  
Application(s):  
(if any)

(Application Number)	(Filing Date)
(Application Number)	(Filing Date)

All Foreign Applications, if any, for any Patent or Inventor's Certificate Filed More than 12 Months (6 Months for Designs) Prior to the Filing Date of This Application:

Country	Application Number	Date of Filing (Month/Day/Year)

Insert Requested  
Information  
(if appropriate)

I hereby claim the benefit under Title 35, United States Code, §120 of any United States and/or PCT application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States and/or PCT application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to the patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

Insert Prior U.S.  
Application(s):  
(if any)

(Application Number)	(Filing Date)	(Status - patented, pending, abandoned)
(Application Number)	(Filing Date)	(Status - patented, pending, abandoned)

I hereby appoint the following attorneys to prosecute this application and/or an international application based on this application and to transact all business in the Patent and Trademark Office connected therewith and in connection with the pending patent based on instructions received from the entity who first sent the application papers to the attorneys identified below, unless the inventor(s) or assignee provides said attorneys with a written notice to the contrary:


Raymond C. Stewart (Reg. No. 21,066)	Terrell C. Birch (Reg. No. 19,382)
Joseph A. Kolasch (Reg. No. 22,463)	James M. Slattery (Reg. No. 24,380)
Bernard L. Sweeney (Reg. No. 24,448)	Michael K. Muter (Reg. No. 29,880)
Charles Garavito (Reg. No. 25,271)	Gerald M. Murphy, Jr. (Reg. No. 28,272)
Leonard R. Swenson (Reg. No. 30,330)	Terry L. Clark (Reg. No. 32,644)
Andrew D. Meikle (Reg. No. 32,868)	Maie S. Weiner (Reg. No. 32,811)
Joe McKinney Murcy (Reg. No. 32,334)	Donald J. Dalry (Reg. No. 34,313)
John W. Bailey (Reg. No. 32,881)	John A. Casellano (Reg. No. 35,094)
Gary D. Yacura (Reg. No. 35,426)	
Pamela A. Ruest (Reg. No. 40,795)	

Send Correspondence to:

**BIRCH, STEWART, KOLASCH & BIRCH, LLP** or Customer No. 2292  
P.O. Box 747 • Falls Church, Virginia 22040-0747  
Telephone: (703) 205-8000 • Facsimile: (703) 205-8050

PLEASE NOTE  
YOU MUST  
COMPLETE  
THE  
FOLLOWING:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and further that these statements were made with the knowledge that willful false statements and the like are made punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

GIVEN NAME/FAMILY NAME Thomas JACKSON		INVENTOR'S SIGNATURE 	DATE 5/2/01
Residence (City, State & Country) State College, Pennsylvania USA		CITIZENSHIP US	
MAILING ADDRESS (Complete Street Address including City, State & Country) 134a Deerfield Drive, State College, Pennsylvania 16801 USA			
GIVEN NAME/FAMILY NAME Jiarne WANG		INVENTOR'S SIGNATURE	DATE
Residence (City, State & Country) Waltham, Massachusetts USA		CITIZENSHIP Chinese	
MAILING ADDRESS (Complete Street Address including City, State & Country) 5 Dolores Avenue, Apt. 8, Waltham, Massachusetts 02452 USA			
GIVEN NAME/FAMILY NAME		INVENTOR'S SIGNATURE	DATE
Residence (City, State & Country)		CITIZENSHIP	
MAILING ADDRESS (Complete Street Address including City, State & Country)			
GIVEN NAME/FAMILY NAME		INVENTOR'S SIGNATURE	DATE
Residence (City, State & Country)		CITIZENSHIP	
MAILING ADDRESS (Complete Street Address including City, State & Country)			

Attorney Docket No. 3672-0111P

**BIRCH, STEWART, KOLASCH & BIRCH, LLP**

P.O. Box 947 • Falls Church, Virginia 22040-0747

Telephone: (703) 205-8000 • Facsimile: (703) 205-8050

PLEASE NOTE:  
YOU MUST  
COMPLETE THE  
FOLLOWING**COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR PATENT AND DESIGN APPLICATIONS**

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated next to my name, that I verily believe that I am the original, first and sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Invent Title

A MEANS FOR ELECTRICAL CONTACTING OR ISOLATION OF ORGANIC OR INORGANIC SEMICONDUCTORS AND A METHOD FOR ITS FABRICATION

Fill in Appropriate

the specification of which is attached herein. If not attached herein,

Information:

file specification was filed on February 28, 2000

For Use Without

United States Application Number 09/765,948

Specification

and amended on \_\_\_\_\_

(if applicable) and/or

Attached:

the specification was filed on June 30, 2000

as PCT

International Application Number PCT/NO00/00228

and was amended under PCT Article 19 on \_\_\_\_\_

(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representative or assigns more than twelve months (six months for designs) prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows:

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

**Prior Foreign Application(s)****Priority Claimed**

Insert Priority

Information:

(if appropriate)

19990366

Norway

June 30, 1999

(Number)

(Country)

(Month/Day/Year Filed)

☒

Yes

☐

No

(Number)

(Country)

(Month/Day/Year Filed)

☐

Yes

☐

No

(Number)

(Country)

(Month/Day/Year Filed)

☐

Yes

☐

No

(Number)

(Country)

(Month/Day/Year Filed)

☐

Yes

☐

No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

Insert Provisional

Application(s):

(if any)

(Application Number)

(Filing Date)

(Application Number)

(Filing Date)

All Foreign Applications, if any, for any Patent or Inventor's Certificate Filed More than 12 Months (6 Months for Designs) Prior to the Filing Date of This Application:

Country

Application Number

Date of Filing (Month/Day/Year)

Insert Requested

Information:

(if appropriate)

I hereby claim the benefit under Title 35, United States Code, §120 of any United States and/or PCT application(s) listed below and, if so, as the subject matter of each of the claims of this application is not disclosed in the prior United States and/or PCT application in the manner provided by the first paragraph of Title 35, United States Code, §112. I acknowledge the duty to disclose information which is material to the patentability as defined in Title 37, Code of Federal Regulations, §1.56 which become available between the filing date of the prior application and the national or PCT international filing date of this application.

Insert Prior U.S.

Application(s):

(if any)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

## Attorney Docket No. 3672-0111P

I hereby appoint the following attorneys to prosecute this application and/or an international application based on this application and to transact all business in the Patent and Trademark Office connected therewith and in connection with the resulting patent based on instructions received from the entity who first sent the application papers to the attorneys identified below, unless the inventor(s) or assignee provides said attorneys with a written notice to the contrary:

Raymond C. Stewart  
Joseph A. Kolasch  
Bernard L. Sweeney  
Charles Gorenstein  
Leonard R. Svenson  
Andrew D. Meikle  
Joe McKinney Murcy  
John W. Bailey  
Gary D. Yacura

(Reg. No. 21,066)  
(Reg. No. 22,463)  
(Reg. No. 24,448)  
(Reg. No. 29,271)  
(Reg. No. 30,330)  
(Reg. No. 32,868)  
(Reg. No. 32,354)  
(Reg. No. 32,881)  
(Reg. No. 35,416)

Terrell C. Birch  
James M. Slattery  
Michael K. Mutter  
Gerald M. Murphy, Jr.  
Terry L. Clark  
Marc's Walner  
Donald J. Delacy  
John A. Castillano

(Reg. No. 19,382)  
(Reg. No. 28,380)  
(Reg. No. 29,680)  
(Reg. No. 28,977)  
(Reg. No. 32,644)  
(Reg. No. 32,181)  
(Reg. No. 34,313)  
(Reg. No. 35,094)

Send Correspondence to:

**BIRCH, STEWART, KOLASCH & BIRCH, LLP**

or

**Customer No. 2292**

P.O. Box 747 • Falls Church, Virginia 22040-0747

Telephone: (703) 205-8000 • Facsimile: (703) 205-8050

PLEASE NOTE:  
YOU MUST  
COMPLETE  
THE  
FOLLOWING:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

<b>GIVEN NAME/FAMILY NAME</b>	<b>INVENTOR'S SIGNATURE</b>	<b>DATE*</b>
Thomas JACKSON		
<b>Residence (City, State &amp; Country)</b>	<b>CITIZENSHIP</b>	
State College, Pennsylvania USA	US	
<b>MAILING ADDRESS (Complete Street Address including City, State &amp; Country)</b>		
1348 Deerfield Drive, State College, Pennsylvania 16801 USA		
<b>GIVEN NAME/FAMILY NAME</b>	<b>INVENTOR'S SIGNATURE</b>	<b>DATE*</b>
Jianna WANG	Jianna Wang	5-15-2001
<b>Residence (City, State &amp; Country)</b>	<b>CITIZENSHIP</b>	
Waltham, Massachusetts USA	Chinese	
<b>MAILING ADDRESS (Complete Street Address including City, State &amp; Country)</b>		
5 Dolores Avenue, Apt. 8, Waltham, Massachusetts 02452 USA		
<b>GIVEN NAME/FAMILY NAME</b>	<b>INVENTOR'S SIGNATURE</b>	<b>DATE*</b>
<b>Residence (City, State &amp; Country)</b>	<b>CITIZENSHIP</b>	
<b>MAILING ADDRESS (Complete Street Address including City, State &amp; Country)</b>		
<b>GIVEN NAME/FAMILY NAME</b>	<b>INVENTOR'S SIGNATURE</b>	<b>DATE*</b>
<b>Residence (City, State &amp; Country)</b>	<b>CITIZENSHIP</b>	
<b>MAILING ADDRESS (Complete Street Address including City, State &amp; Country)</b>		

\*DATE OF SIGNATURE